

**REMARKS/ARGUMENTS**

The Examiner has rejected claims 1-6, 12-16, and 25-27 under 35 U.S.C. 103(a) as being unpatentable over Herrera (US 6,208,350). Based on Examiner's comments, it is not clear if this rejection should properly be under 35 USC 103 or under 35 USC 102, however Applicant's comments below respond to both types of rejections.

By this amendment, claims 1, 2, 12, 14-16, 26 and 27 have been amended to clarify the scope of the claims and to correct grammatical errors. Claim 13 has been cancelled without prejudice.

Applicant submits that Herrera discloses the allocation of tasks in DVD rendering between the main CPU and a 3D graphics accelerator. In particular, Herrera deals with decreasing the load on the CPU by making changes to the architecture of the 3D graphics accelerator. In Herrera, texture mapping engine 110 has been adapted to perform motion compensation and other functions in the MPEG procedure in addition to conventional texture mapping. Further, raster operations 114 and pixel packing logic 116 have been modified to support the additional functions of texture mapping engine 110.

The Examiner has indicated that the texture mapping engine 110 and the raster operations 114 of Herrera generally correspond to the performance detecting (specifying) unit and the resolution decision unit of, for example, claim 12 in the present application.

Applicant has reviewed Herrera and has not found any indication that the texture mapping engine 110, or any other elements, detect the processing performance of the apparatus for generating an intermediate image. Applicant submits that, since Herrera

deals with modifications to a 3D graphic accelerator to allow the 3D graphic accelerator to take some of the processing load from any CPU, there is no need to detect the processing performance of the apparatus. Further, Herrera does not seem to deal with adjusting resolution, either in the raster operations 114 or in other elements, other than perhaps peripherally. In particular, since Herrera does not teach or suggest the detection of processing performance, Herrera also does not teach or suggest determining resolution of intermediate frames to be generated in accordance with the detected processing performance or conversion processing of the key frames and the corresponding point data in accordance with the determined resolution.

Applicant also submits that the objectives of Herrera are different than those of the present application. Herrera is directed to increasing the processing performance of a DVD decoding apparatus, whereas the present application is directed to reproducing a motion picture at an appropriate image quality for each type of apparatus regardless of processing performance. Applicant submits that these differing objectives are at cross purposes and Herrera might lead one of skill in the art away from the elements of the claims in the present application.

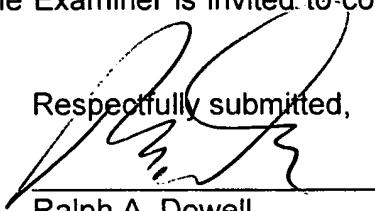
As noted above, Applicant submits that Herrera does not teach or suggest at least the element of "detecting a processing performance of an apparatus which generates the intermediate frames" as claimed in claim 1, and similarly claimed in claim 12 and 26. As such, Applicant submits that claims 1, 12 and 26 are in condition for allowance.

Claims 2-6, 14-16 and 27 depend from claims 1, 12 and 26 and, for at least similar reasons, as well as the additional elements therein, are also believed to be in condition for allowance.

**CONCLUSION**

In view of the foregoing comments, it is respectfully submitted that the application is now in condition for allowance. Favourable action on this application is respectfully requested. If the Examiner has any further concerns regarding the language of the claims or the applicability of the cited references, the Examiner is invited to contact the undersigned.

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Respectfully submitted,

Ralph A. Dowell  
Registration No. 26868  
Dowell & Dowell, P.C.  
2111 Eisenhower Ave. Suite 406  
Alexandria, VA 22314  
Telephone: 703-415-2555